

APR 03 2002

Substitute for Form PTO-1449

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Complete if known

Sheet	1	of	1	Attorney Docket Number	21891.03200
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Application Number	09/670,911
First Named Inventor	Sherry Solden et al.
Group Art Unit	Not yet assigned
Examiner Name	Not yet assigned

COPY OF PAPERS
ORIGINALLY FILED**U.S. PATENT DOCUMENTS**

Examiner Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant Of Cited Document	Date of Publication of Cited Document MM- DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
	A1	5,870,588		Rompaey et al.	02/09/1999	
	A2	6,026,219		Miller et al.	02/15/2000	

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FOREIGN PATENT DOCUMENTS

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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T*
	C1	Li, J.J. (1998) "Performance Prediction Based on Semi-Formal Software Architectural Description," <i>Performance, Computing and Communications, IEEE International pp.</i> 193-199	
	C2	Lajolo, M. et al. (1998) "A Case Study on Modeling Shared Memory Access Effects During Performance Analysis of HW/SW Systems," <i>Hardware/Software Codesign, Proc. of the 6th International Workshop on Hardware/Software Codesign</i> , pp:15-18	
	C3	Lahiri, K. et al. (1999) "Fast Performance Analysis of Bus-Based System-on-Chip Communication Architectures," <i>International Conference on Computer Aided Design, Proc. of the 1999 International Conference on Computer-Aided Design</i> , pp. 566-573	
	C4	Yoo, S. et al. (2000) "Performance Estimation of Multiple-Cache IP-based Systems: Case Study of an Interdependency Problem and Application of an Extended Shared Memory Model," <i>International Conference on Hardware Software Codesign, Proc. of the 8th International Workshop on Hardware/Software Codesign</i> , pp: 77-81	

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